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2 Altera Corporation Preliminary Using MAX II CPLDs as Analog Keyboard Encoders Figure 1. Simple One CPLD I/O per Switch Circuit A common way to reduce the number of

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An 500: nand flash memory interface with max ii

NAND Flash Memory Interface with MAX II CPLDs Application Note 500: NAND Flash Memory Interface with Altera MAX II CPLDS Table 1 describes the interfacing signals.

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this causes substantially higher power consumption and may also reduce the offers additional cost Digital Equipment Corporation: System and method

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